

DATA SHEET

IP's USED

- azPHY (PIPE 4.3 compliant PHY model)
- azPIPE (PIPE interface analyzer)
- azMAC (UVC to test the PIPE interface)

PROTOCOLS

- PIPE 4.3
- PCI Express (2.5, 5, 8, 16 Gbps)
- USB (5.0, 10.0 Gbps)
- SATA (1.5, 3,6 Gbps)

DELIVERABLES

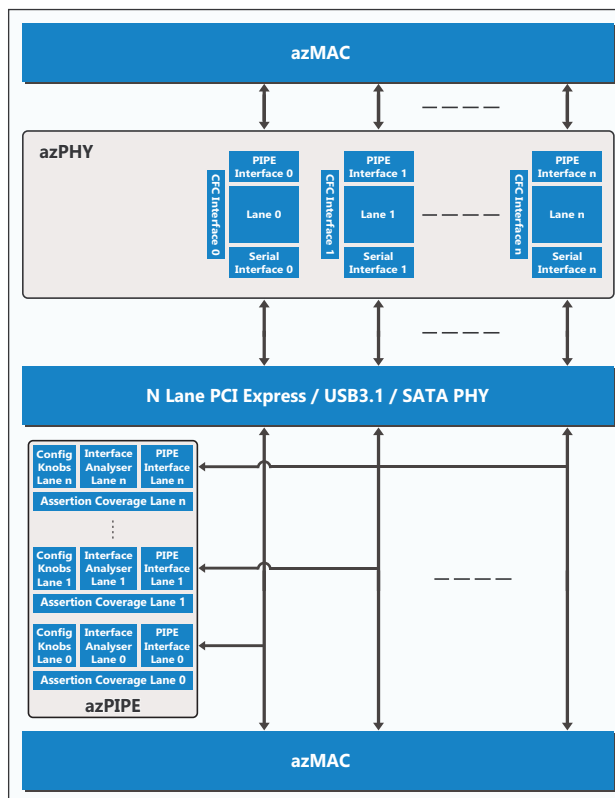
- PHY model
- Back-Back Testbench
- Exhaustive Test Suite
- User Guide

SUPPORT

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CONTACT

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Description

The Compact PHY VIP Solution is a combination of three products (azPHY, azMAC and azPIPE) and provides a complete solution to your PIPE compatible PHY verification needs. azPHY is a systemverilog based PIPE-PHY model. azPIPE is a set of assertions to check the correctness of the PIPE interface. azMAC is a UVC used to drive the PHY at the PIPE interface

Features

- Plug-n-Play PHY verification solution.
- azPHY is fully compatible to PIPE specification version 4.3.
- azPHY supports driving real values on the serial line with variable de-emphasis, preshoot and swing.
- azPIPE is an independent set of assertions and can be plugged in at the PIPE interface.
- azMAC is capable of generating all PHY related scenarios mimicking a PCI Express / SATA / USB controller.
- Error injection on Tx and Rx side.
- Transmitter Equalization support.

Configurability

- Configurable data-rates.
- Configurable data width.
- Configurable number of links and lanes.
- All combinations of data rate, data width and PCLK rate supported.