

## SPECIFICATIONS

- JESD235
- JESD235A

## AGENTS

- Host
- Memory

## DELIVERABLES

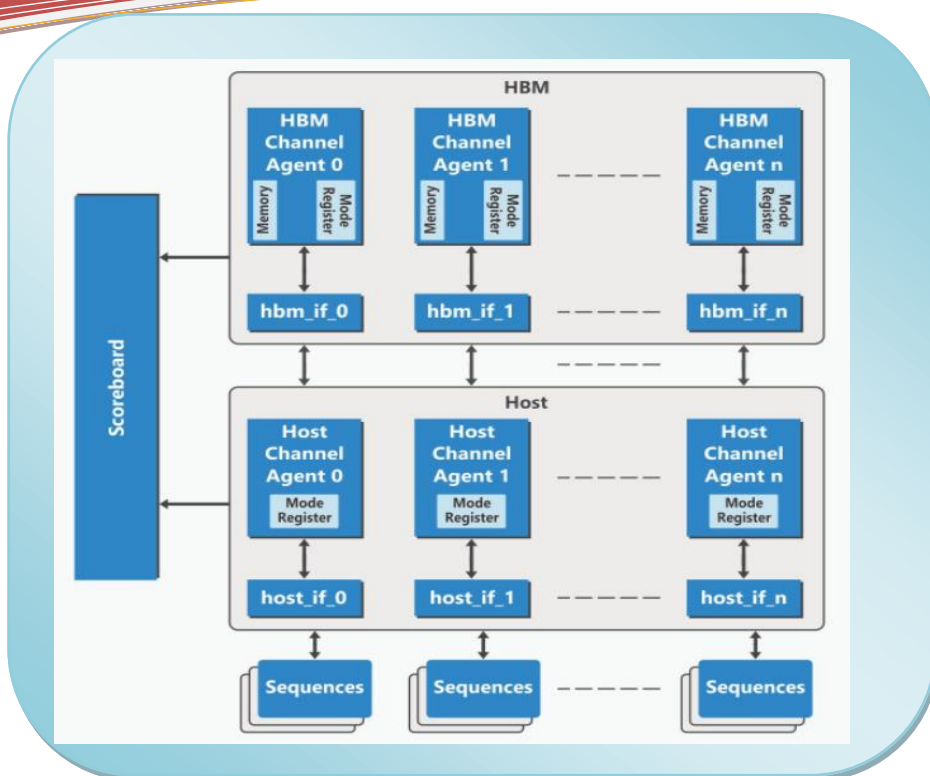
- HBM UVC
- Back-Back Testbench
- Exhaustive Test Suite
- User Guide

## SUPPORT

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## CONTACT

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## Description

The agents in this UVC can be configured to be a HBM host or memory. This can be used to verify a HBM Host or HBM memory design. It supports all memory sizes and is pluggable to user memories also. It supports all mode registers that can be configured by the user.

## Features

- Host and Device agents
- HBM 2.0 specification supported
- Supports legacy mode and pseudo channel mode
- Supports all the delay parameters
- Pipelined transfer support
- Callbacks for error insertion
- Option to send refresh and single bank refresh on its own
- TRR is supported by host engine
- DM and DBI supported
- Trackers to debug transactions on the HBM interface
- Row and column command can be independently driven by user